

Abstracts

A Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses a New Pulse Generator LSI

T. Nakagawa and T. Ohira. "A Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses a New Pulse Generator LSI." 1994 Transactions on Microwave Theory and Techniques 42.12 (Dec. 1994, Part II [T-MTT] (1994 Symposium Issue)): 2579-2582.

A new phase noise reduction technique has been developed for PLL frequency synthesizers. The key circuit elements have been fabricated in one LSI, and applied to a C-band MMIC frequency synthesizer. The resultant phase noise reduction of 10 dB is significant.

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